

# PATENT ABSTRACTS OF JAPAN

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## (54) SOLID-STATE IMAGE-PICKUP DEVICE

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a solid-state image-pickup device capable of realizing a miniaturized and integrated device by reducing the number of external terminals, which is capable of increasing the output operation speed of a pixel signal by reducing the data rate of the pixel signal and readily controlling the signal quantity of a high luminance correction signal and easily controlling a dynamic range.

**SOLUTION:** This solid-state image-pickup device 1 is provided with a pixel signal storing memory circuit 11, a high luminance correction signal storing memory circuit 10 and a compositing circuit 14. the circuit 11 temporarily stores a pixel signal stored by a prescribed pixel P in a pixel area 2. The circuit 10 temporarily stores a high luminance correction signal for correcting high luminance stored by the same prescribed pixel P. The pixel signal stored in the circuit 11 and the high luminance correction signal stored in the circuit 10 are transferred to the circuit 14 at the same timing and synthesized, and a corrected pixel signal is obtained.

